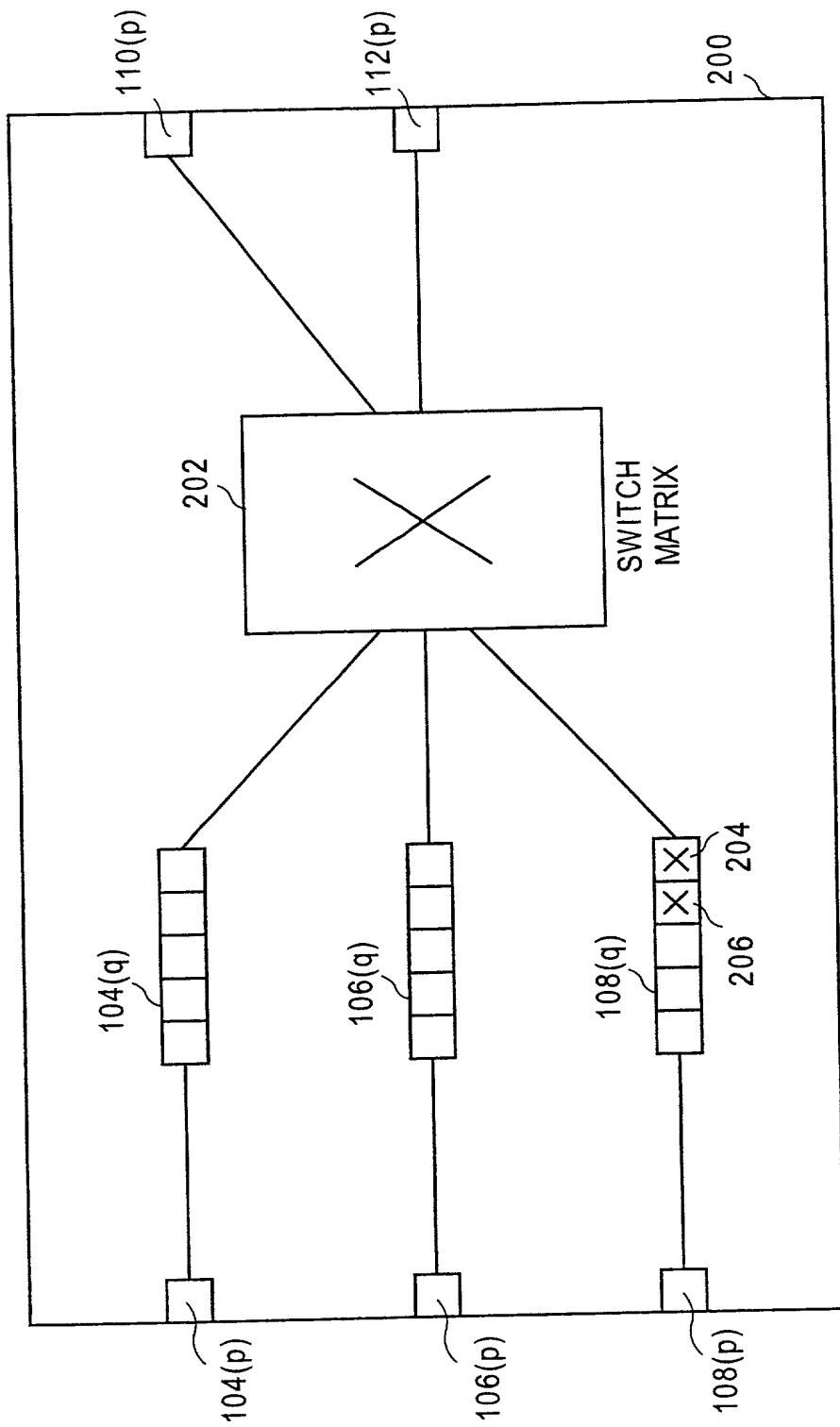


FIG. 1  
(prior art)



INPUT BUFFER SWITCH

FIG. 2A  
(prior art)

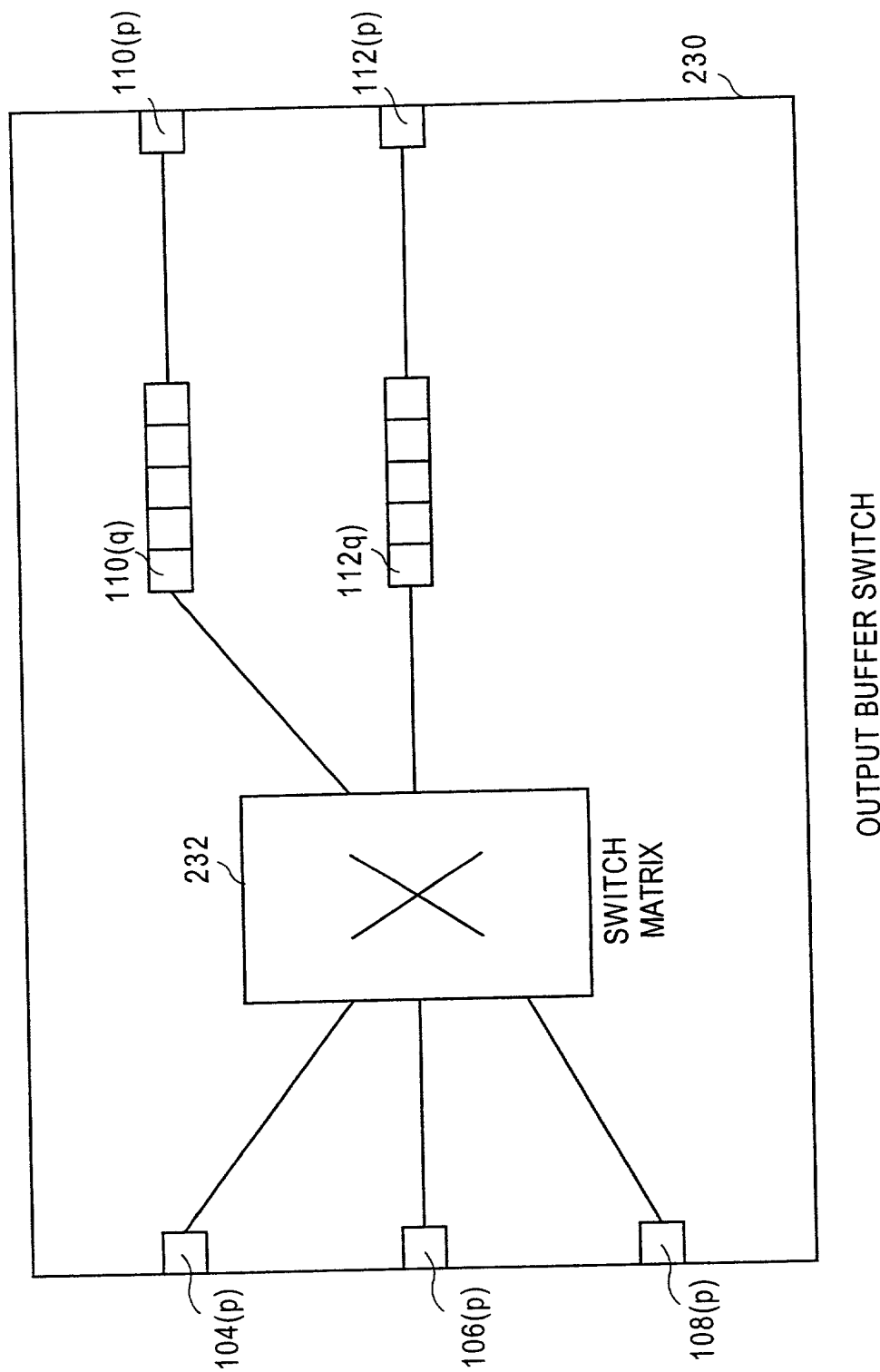
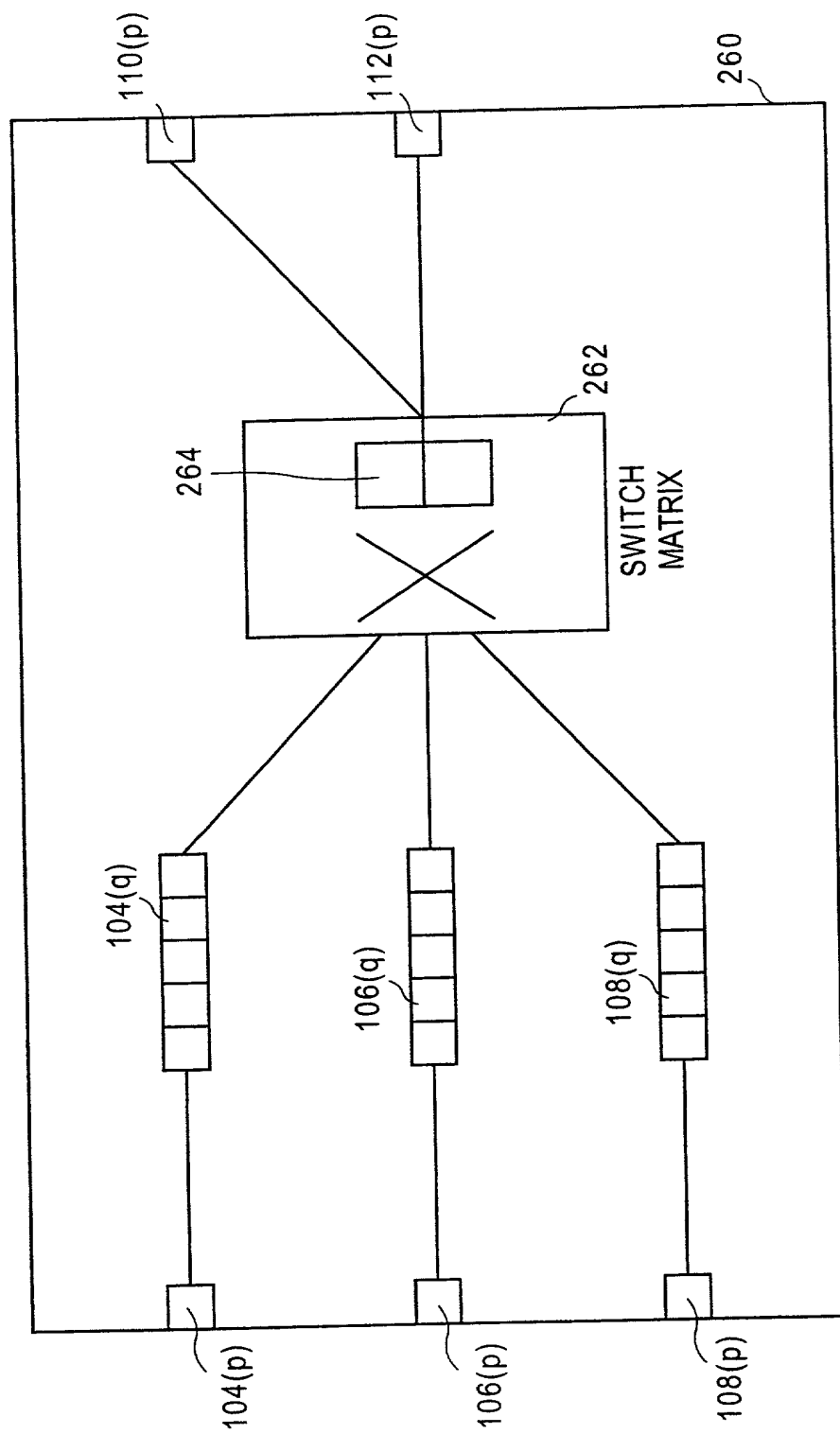


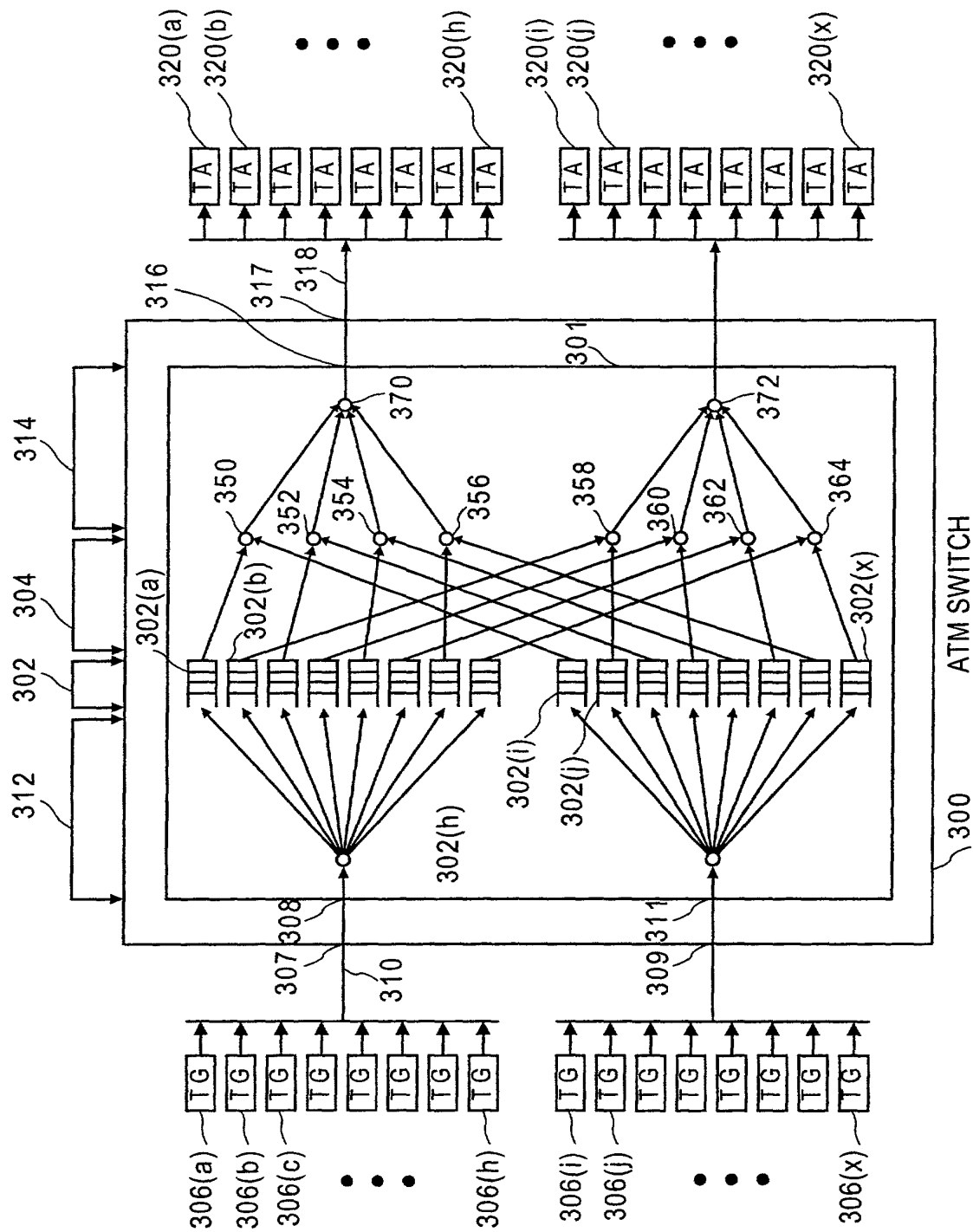
FIG. 2B  
(prior art)



HYBRID SWITCH

FIG. 2C  
(prior art)

FIG. 3



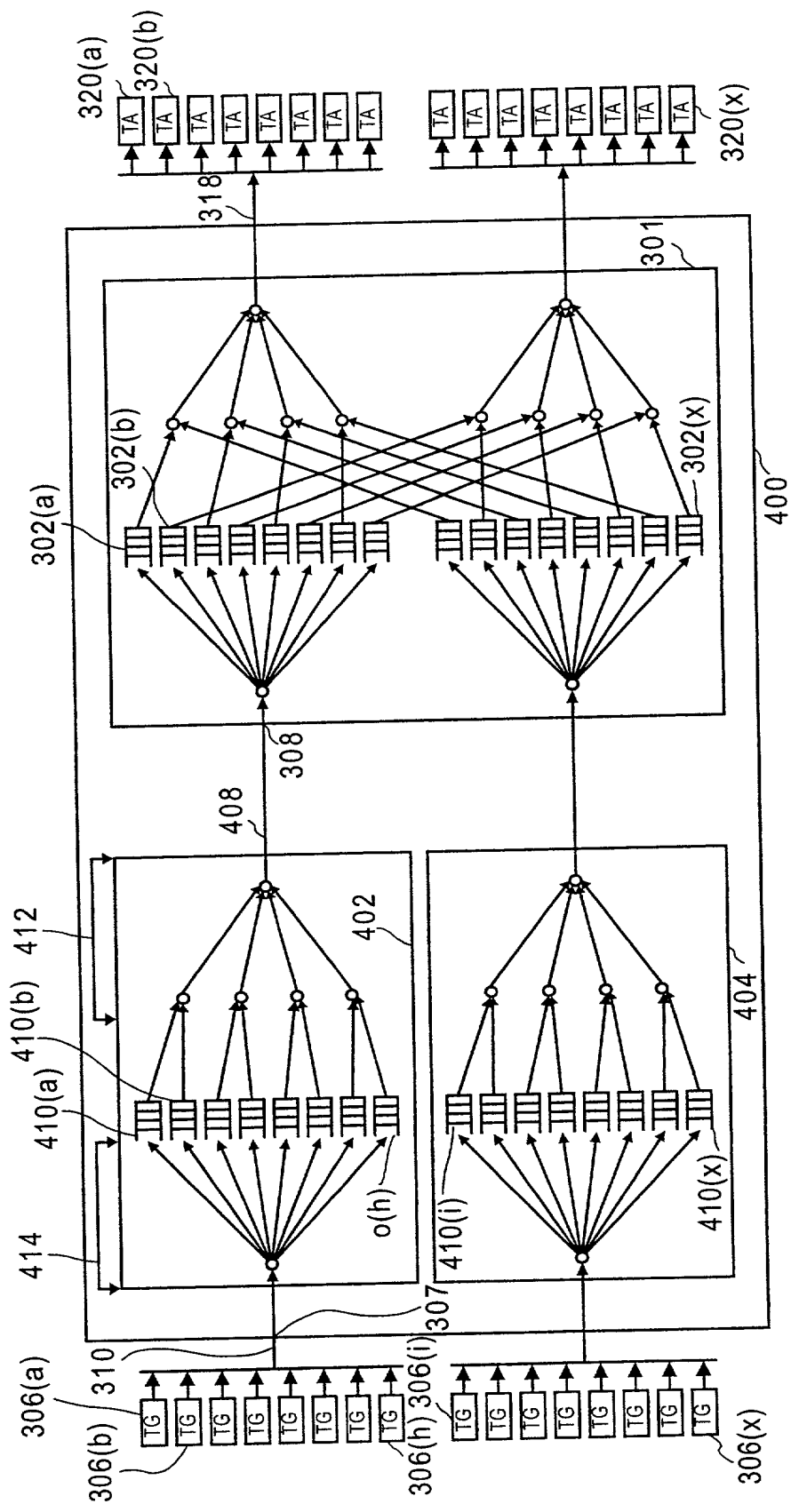


FIG. 4

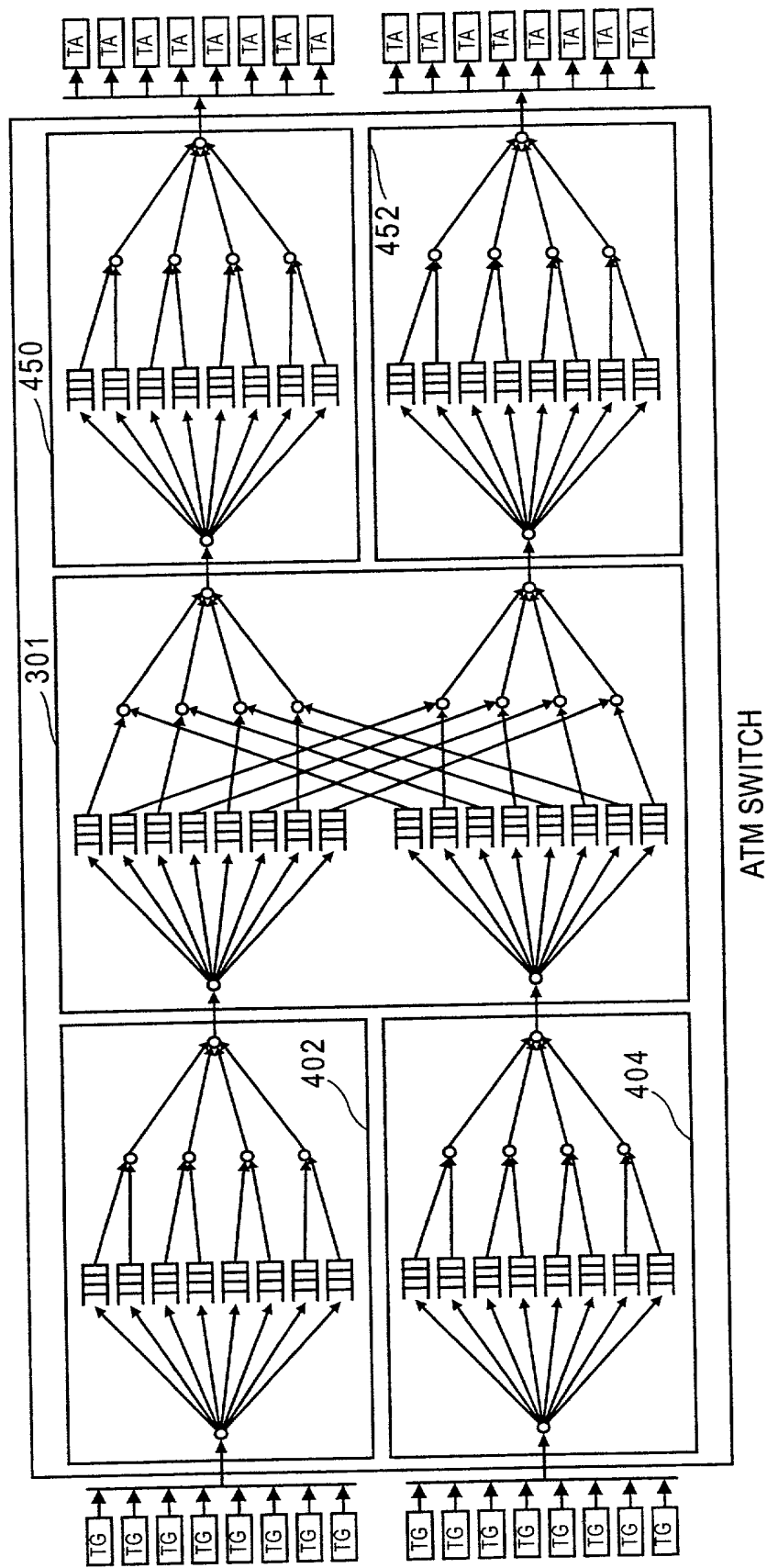


FIG. 5

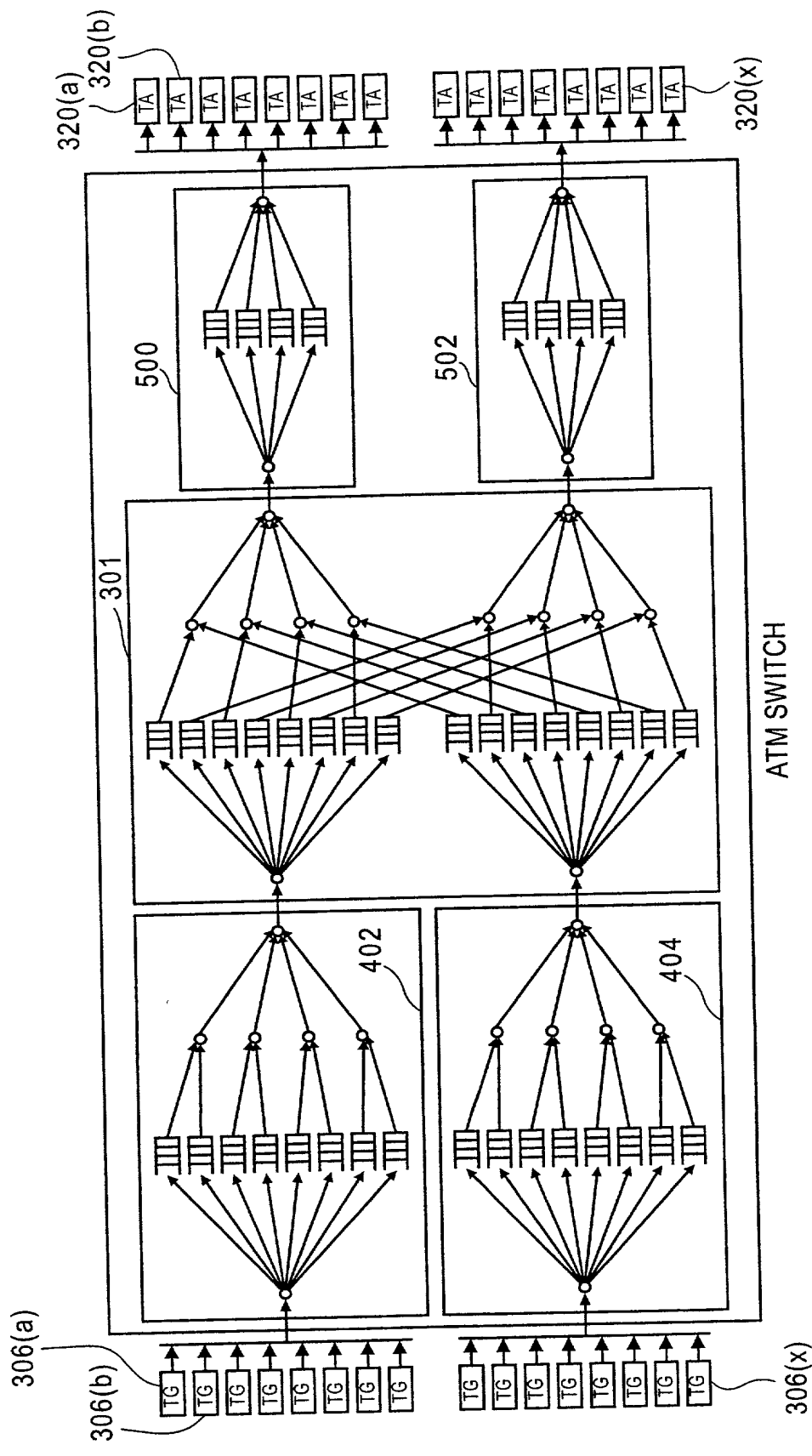


FIG. 6



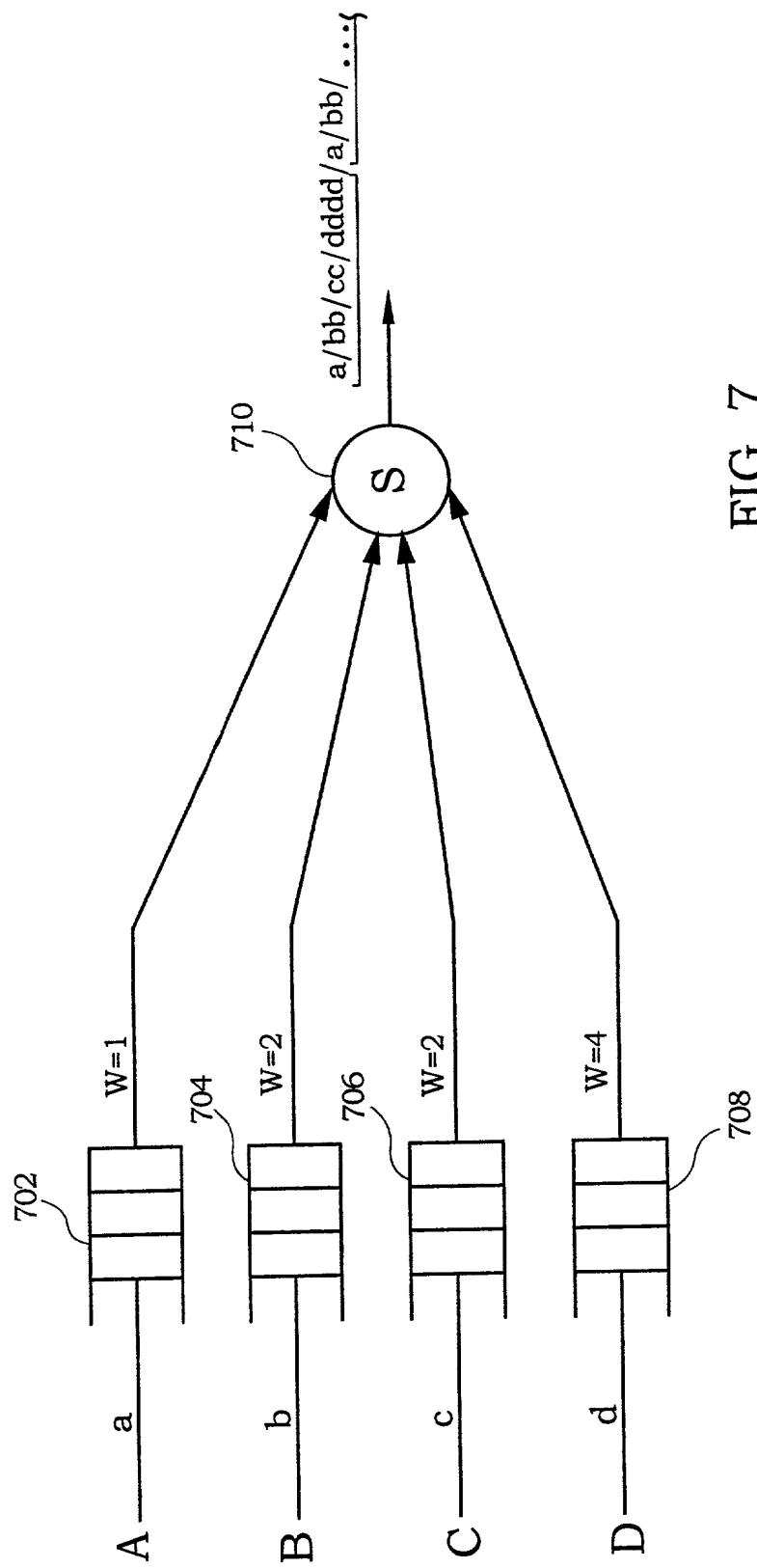


FIG. 7

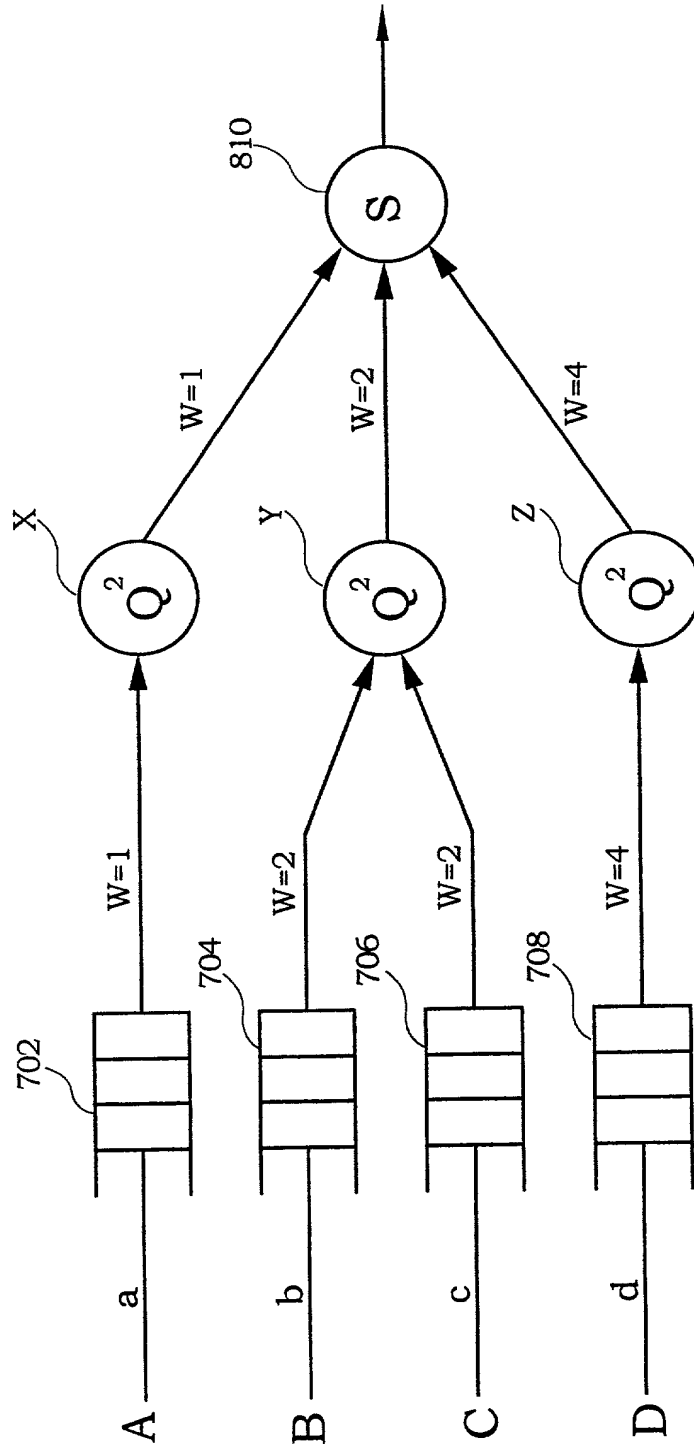


FIG. 8A

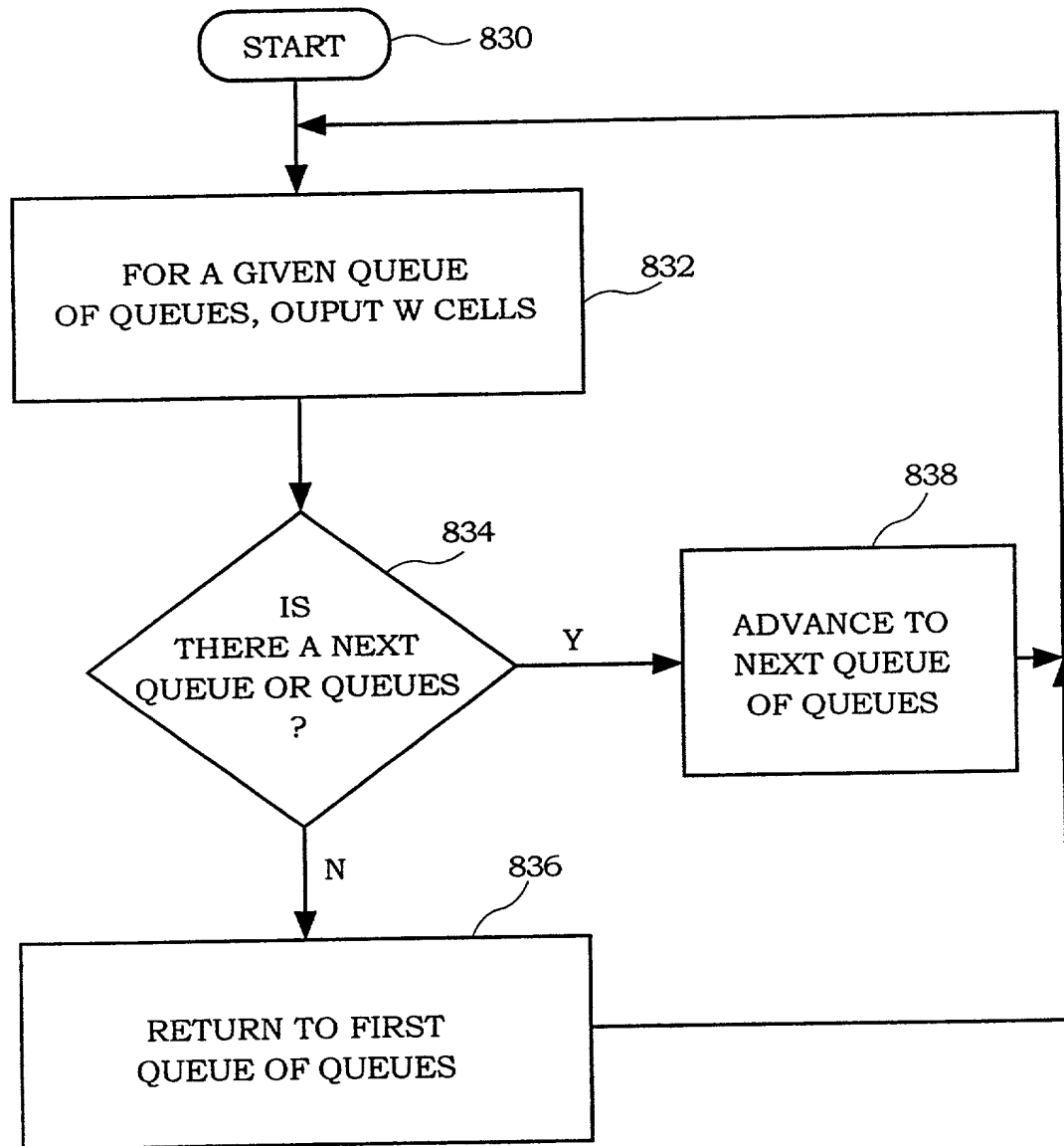


FIG. 8B

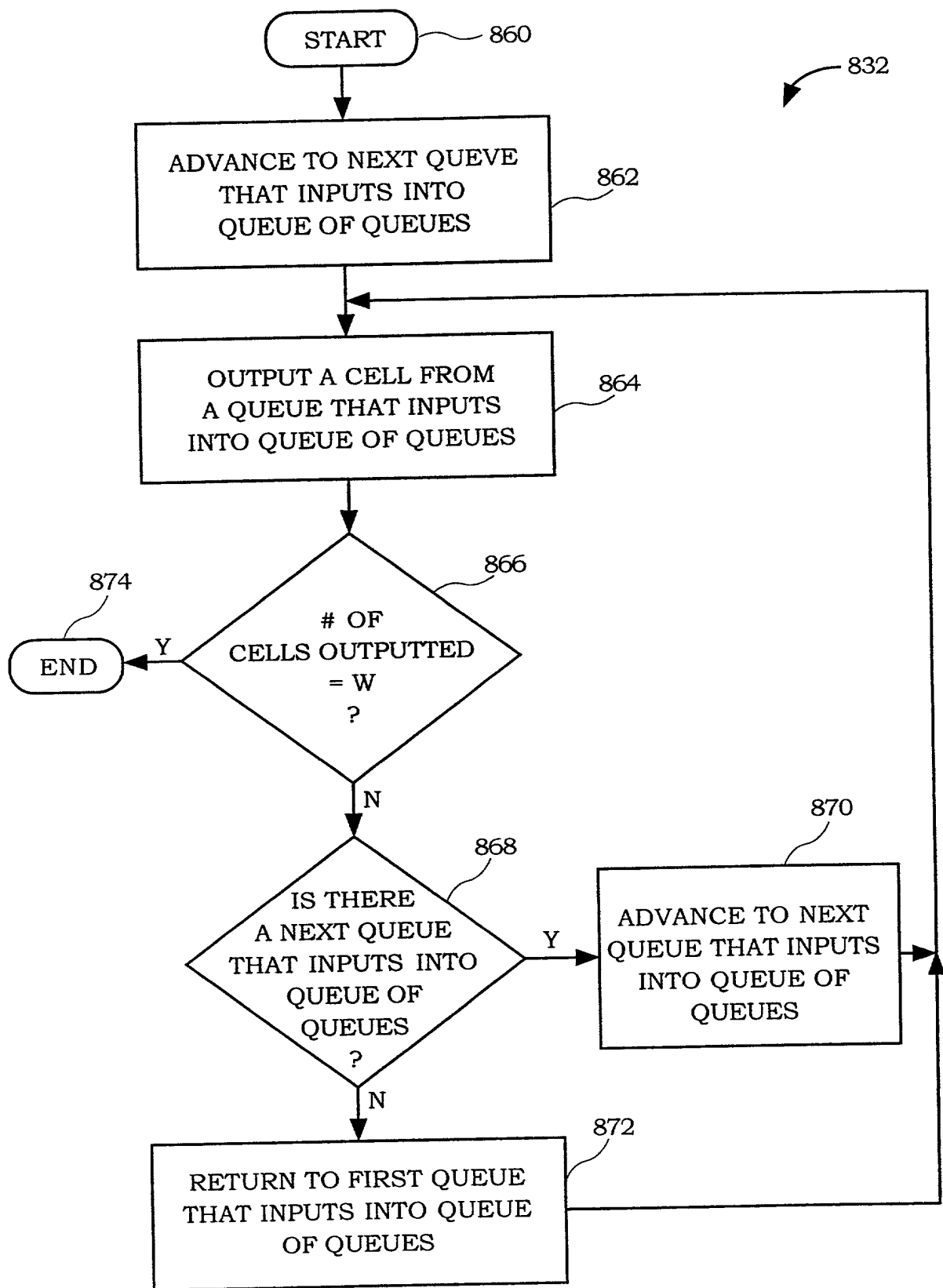


FIG. 8C

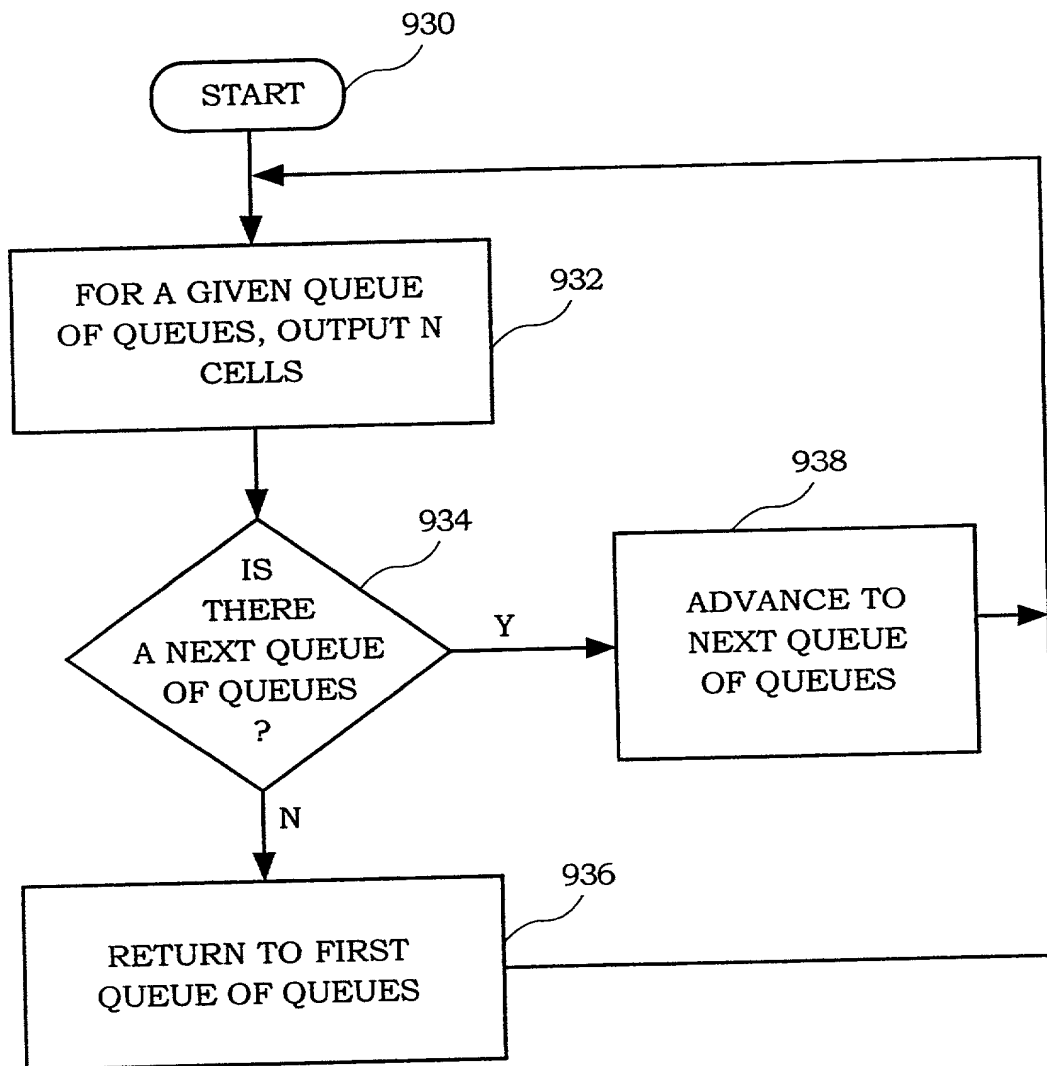


FIG. 9A

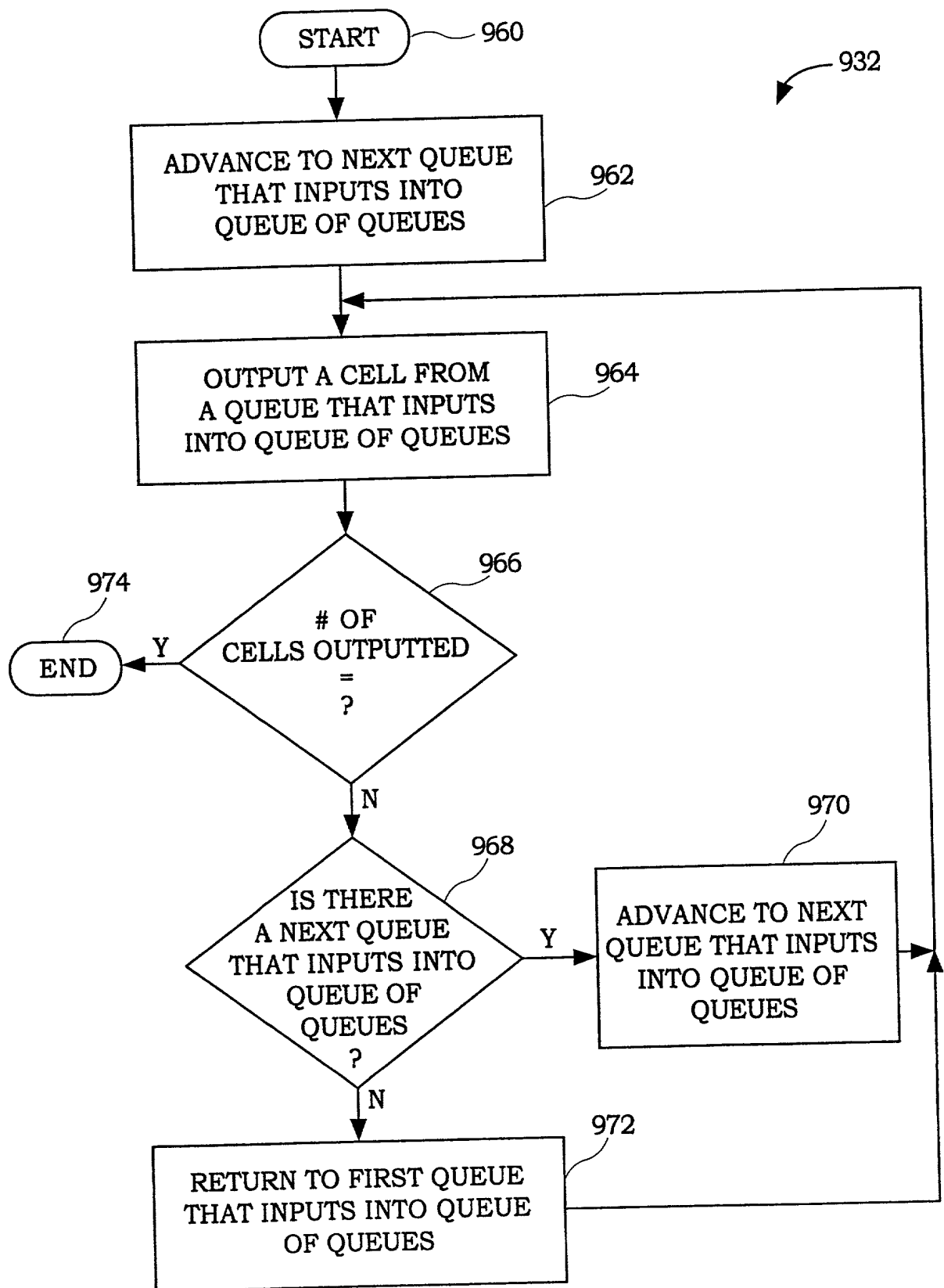


FIG. 9B

Queue of Queue		Dynamic Weight																Row #	
		Static Weight																	
X	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
			2	2	1	1	0	0	0	0	2	1	1	0	0	0	0	0	2
Y	2	2	4	4	4	3	3	2	1	0	4	4	3	3	2	1	1	3	
			4	4	4	3	3	2	1	0	4	4	3	3	2	1	1	3	
Z	4	4																	4
Output			X Y Z Y Z Z Z X Y Z Y Z Z																
Column #			1 2 3 4 5 6 7 8 9 10 11 12 13 14																

FIG. 10

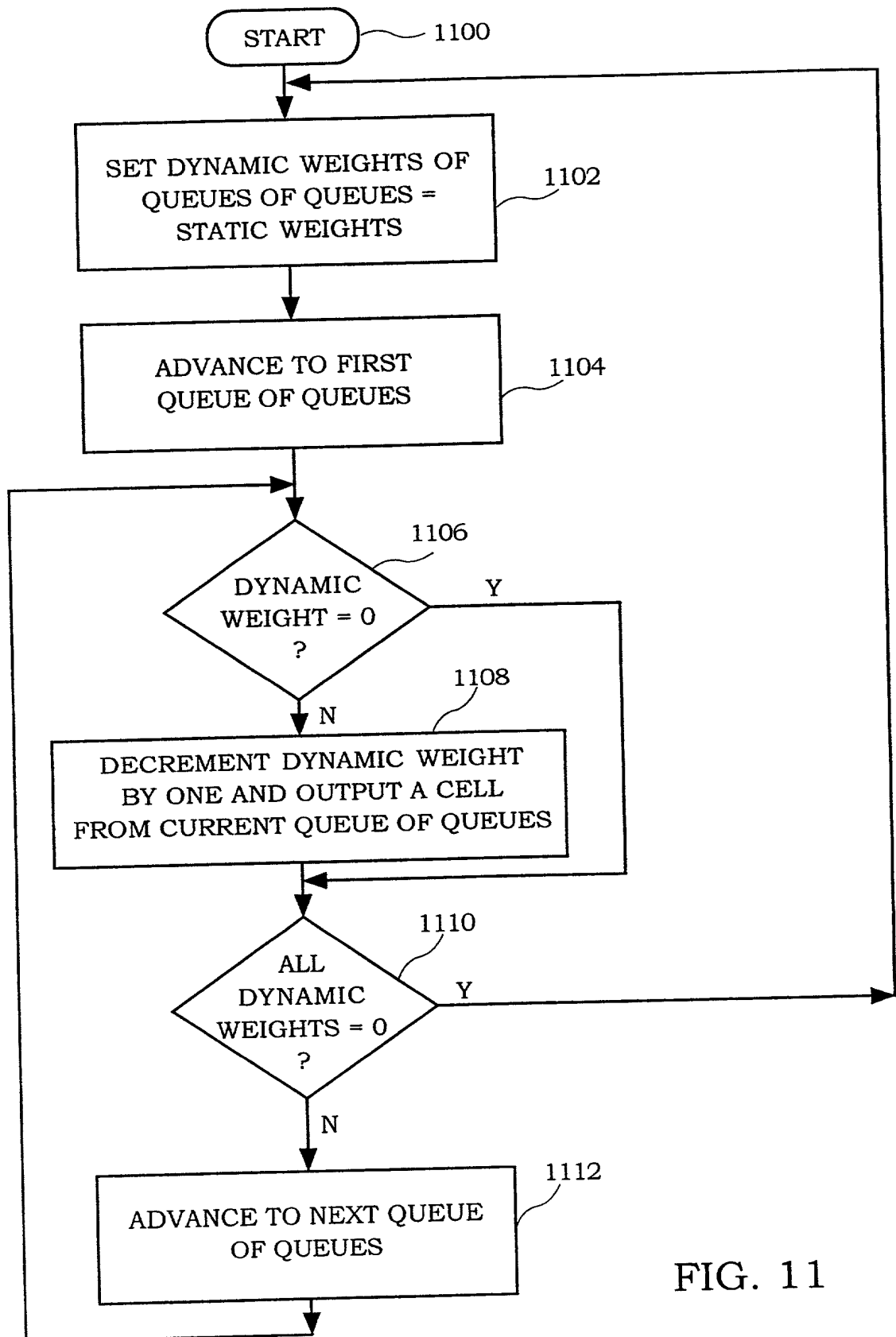


FIG. 11



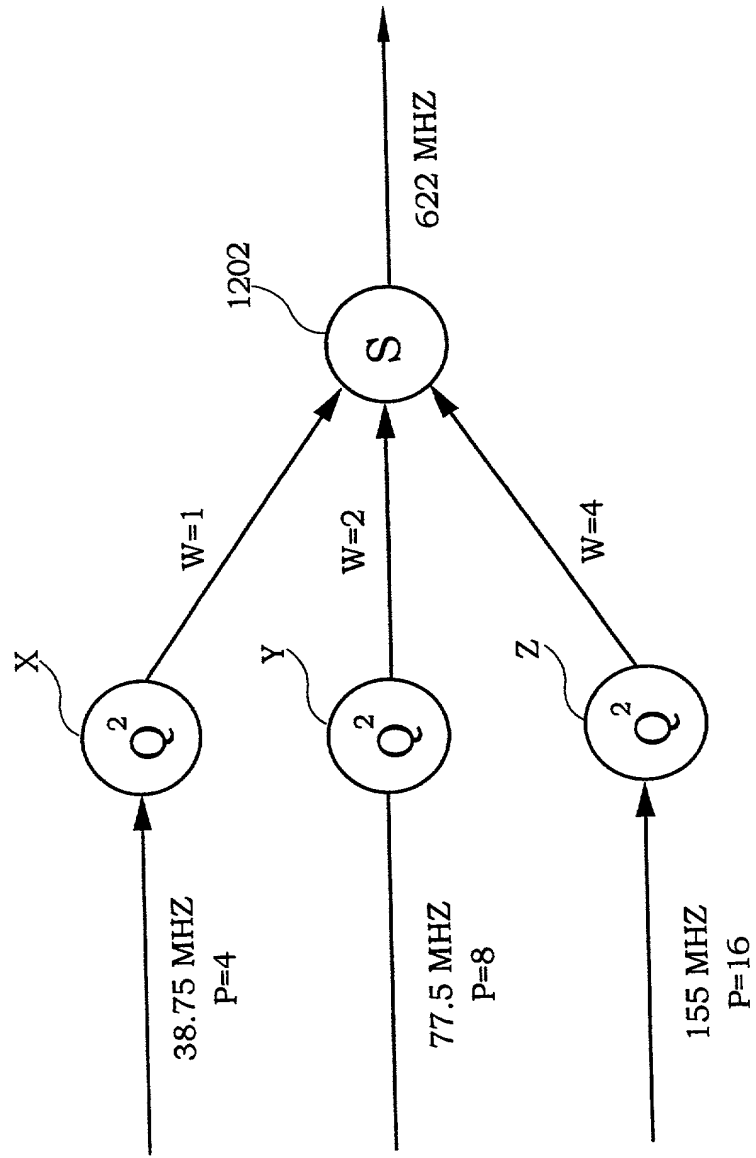


FIG. 12

[illegible]

FIG. 13

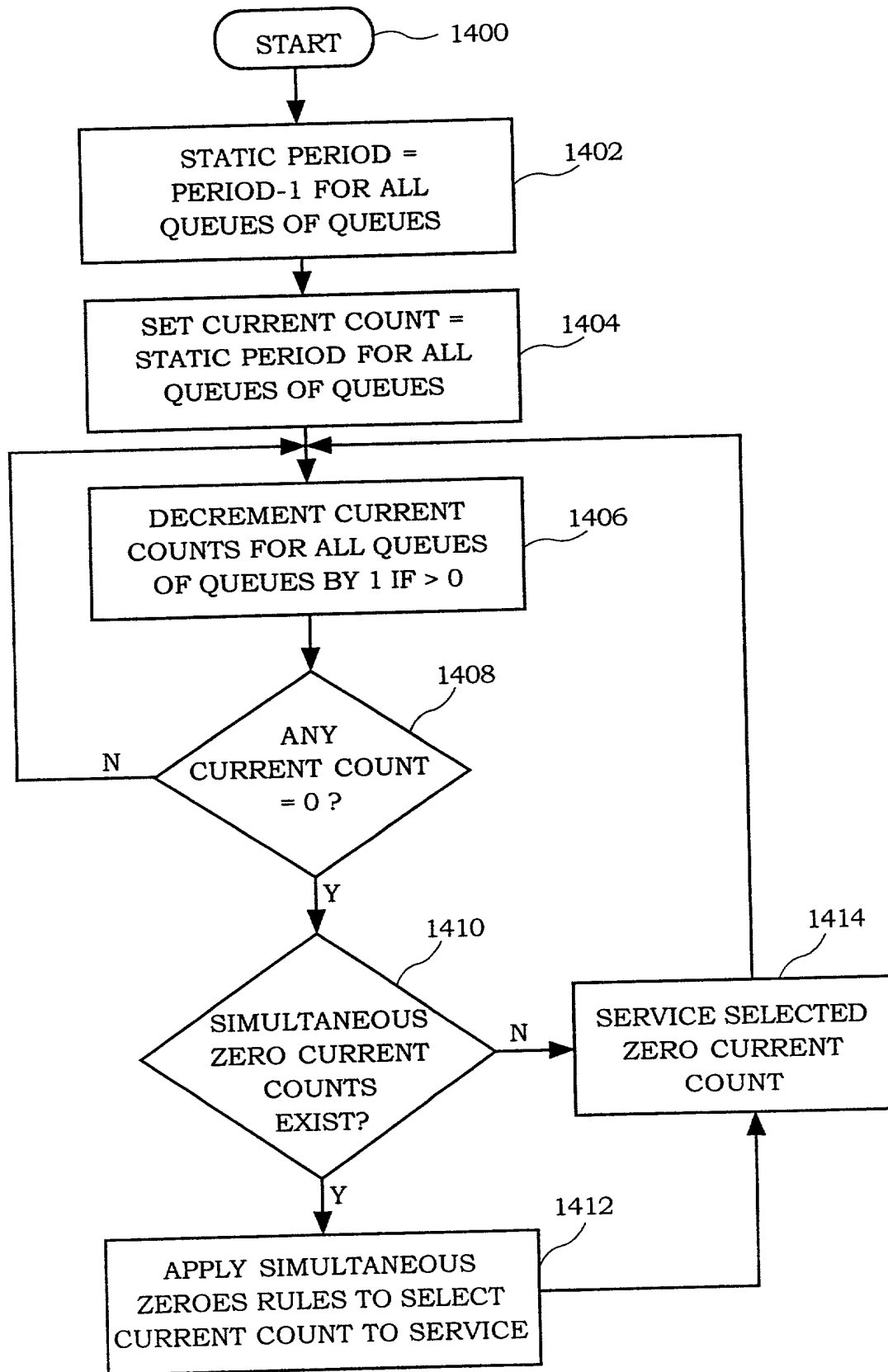


FIG. 14

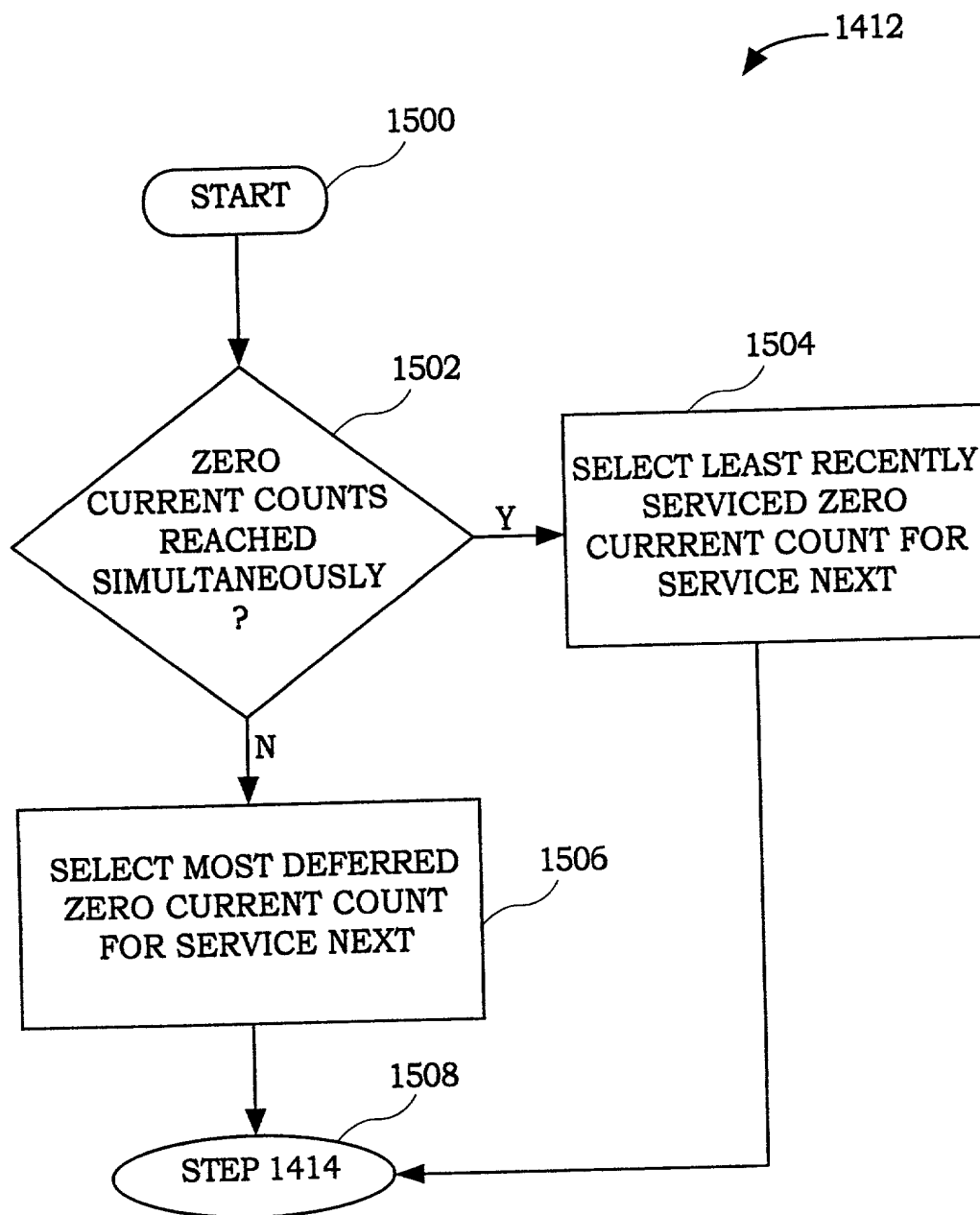


FIG. 15

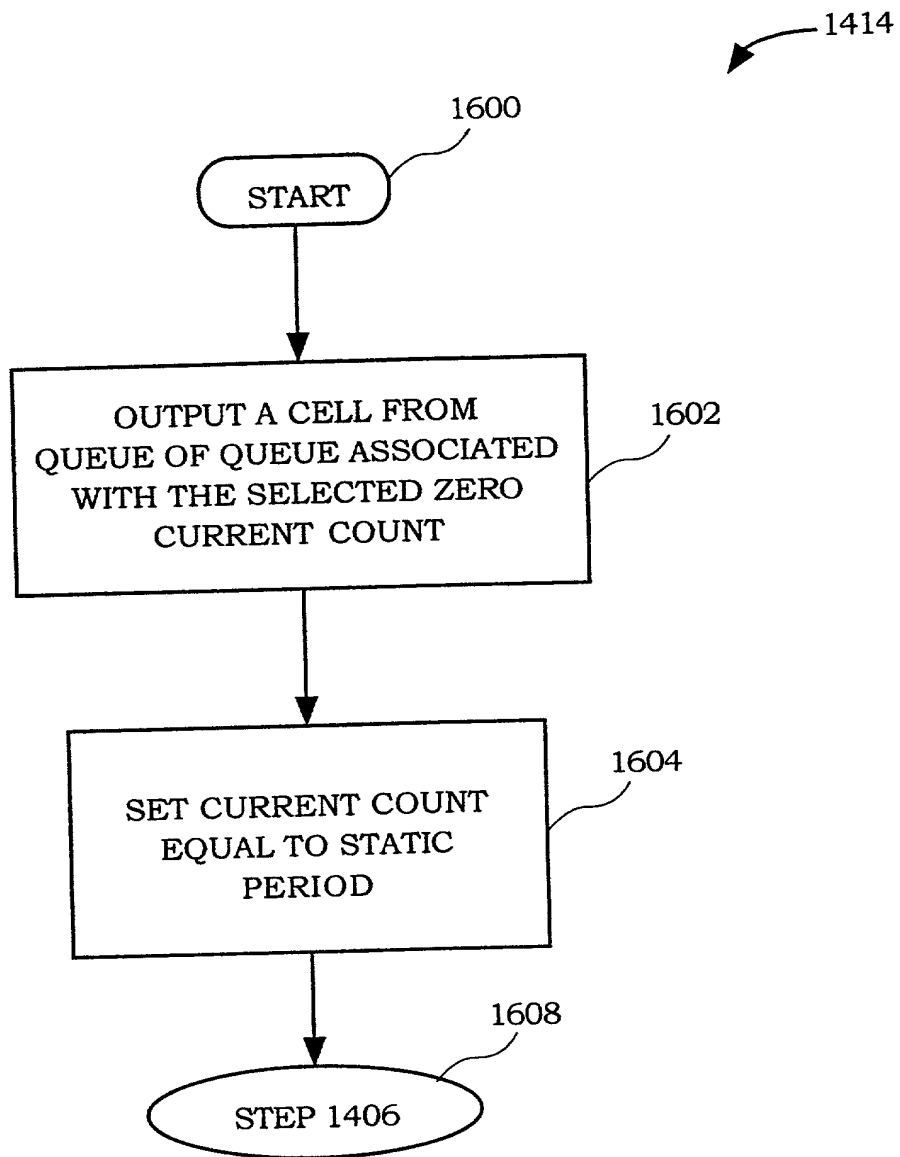


FIG. 16

Queue of Queue		Static Period Current Count																						Row #
X	1	0	1	0	0	1	0	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0		
		2	1	0	3	2	1	0	3	2	1	0	3	2	1	0	3	2	1	0	3	2		
		3	1	0	0	3	2	1	0	0	3	2	1	0	0	3	2	1	0	0	3	2		
Y	3	2	1	0	3	2	1	0	3	2	1	0	3	2	1	0	3	2	1	0	3	2		
		3	1	0	0	3	2	1	0	0	3	2	1	0	0	3	2	1	0	0	3	2		
		3	1	0	0	3	2	1	0	0	3	2	1	0	0	3	2	1	0	0	3	2		
Z	3	2	1	0	3	2	1	0	3	2	1	0	3	2	1	0	3	2	1	0	3	2		
		3	1	0	0	3	2	1	0	0	3	2	1	0	0	3	2	1	0	0	3	2		
		3	1	0	0	3	2	1	0	0	3	2	1	0	0	3	2	1	0	0	3	2		
Output		x		y	z	x		y	x	z	x	y	x	z	x	y	x	z	x	y	x	z		
Column #		C1	C2	C3	C4	C5	C6	C7	C8	C9	C10	C11	C12	C13	C14	C15	C16	C17	C18	C19	C20	C21	C22	

FIG. 17